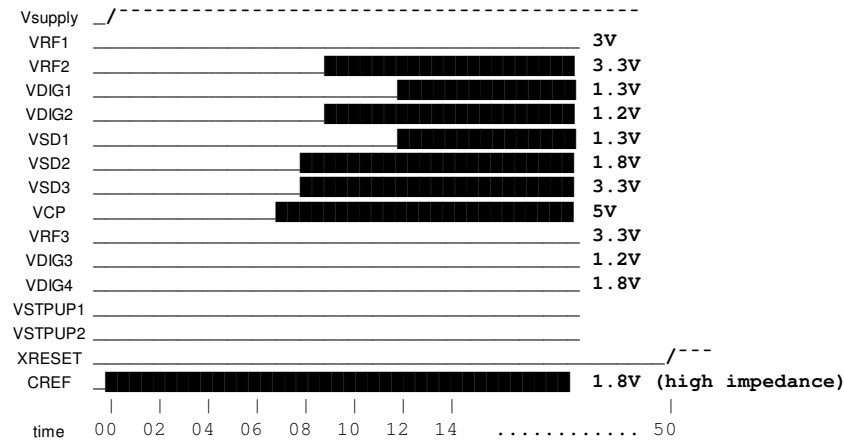


Boot-ROM Mapping Table for Code = xx / please only modify Data-Column

| ROM-Adr. | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | Data | Register | Section | Description |
|----------|----|----|----|----|----|----|----|----|------------------|-------------------------|------------|--|
| 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 14 | Step Down Voltage1 | Bank 1 | clkinv=0; frequ=2.2MHz; VBUCK=1.3V |
| 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 152 | Step Down Voltage2 | | clkinv=1; frequ=2.2MHz; VBUCK=1.8V |
| 2 | 0 | 0 | 1 | 1 | 0 | 1 | 1 | 0 | 54 | Step Down Voltage3 | | clkinv=0; frequ=2.2MHz; VBUCK=3.3V |
| 3 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 23 | LDO_RF1 Voltage | | swprot_en=0; lcurr_en=0; VRF1=3V |
| 4 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 1 | 29 | LDO_RF2 Voltage | | double_reset=0; slow_startup=0; lcurr_en=0; VRF2=3.3V |
| 5 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 1 | 29 | LDO_RF3 Voltage | | rf3_hotplug_en=0; lcurr_en=0; VRF3=3.3V |
| 6 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 11 | LDO_DIG1 Voltage | | VDIG1=1.3V |
| 7 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 9 | LDO_DIG2 Voltage | | VDIG2=1.2V | |
| 8 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 9 | LDO_DIG3 Voltage | | VDIG3=1.2V | |
| 9 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 1 | 25 | LDO_DIG4 Voltage | | VDIG4=1.8V |
| 10 | 1 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 216 | USB Charger Control | | ext_batsw_en=1; no_charging=1; dis_batsw_temp_prot=0; usb_chg_en=1; usb_current=470mA |
| 11 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 173 | Charger Control1 | | isolate_bat=1; ch_det_500ms=0; chg_tmax=1; usb_hold_chdet=0; autoresume=1; CHOVDetEn=1; Ch_pwoff_en=0; ChEn=1; |
| 12 | 0 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 103 | Battery voltage monitor | | FastResEn=0; sup_res_en=1; ResFall=3.1V; ResRise=3.4V; |
| 13 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 0 | 38 | Charger Config | | ChVoltResume=3.9V; Vchargeoff=3.9V; chvoltageoc=4.2V; |
| 14 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Charger supervision | | ntc_type=0; ntc_hyst=0; ntc_hightemp=0; autoshtutdown=0; ch_timeout=off |
| 15 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | FuelGauge | | ntc_on=Disable NTC supervision; power_off_at_vsuplow=0; CalMod=0; CalReq=0; UpdReq=0; FGEN=0; |
| 16 | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 61 | ChargerCurrent | | Ch_Voltage=4.4V; const_current=35mV; TrickleCurrent=2.5mV; |
| 17 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | Charge Pump Control | | SD1ctrl=no 1A mode; sd1_dvm_time=0usec; frequ=1.1MHz; Pulseskip=1; |
| 18 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO1 Control | | no pulls; I/O mode; no invert; high impedance |
| 19 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO2 Control | | no pulls; I/O mode; no invert; high impedance |
| 20 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO3 Control | | no pulls; I/O mode; no invert; high impedance |
| 21 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO4 Control | | no pulls; I/O mode; no invert; high impedance |
| 22 | 1 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 139 | Reset Timer | | rtc_mode=Pin Q32k enabled; rtc_rep_wakeup_en=0; rtc_alarm_wakeup_en=0; xon_enable=1; Res_Timer=50ms |
| 23 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 6ms after start |
| 24 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 128 | Reg Power1 Ctrl | | CP ON 7ms after start |
| 25 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 224 | Reg Power1 Ctrl | | CP SD3 SD2 ON 8ms after start |
| 26 | 1 | 1 | 1 | 0 | 1 | 0 | 1 | 0 | 234 | Reg Power1 Ctrl | | CP SD3 SD2 DIG2 RF2 ON 9ms after start |
| 27 | 1 | 1 | 1 | 0 | 1 | 0 | 1 | 0 | 234 | Reg Power1 Ctrl | | CP SD3 SD2 DIG2 RF2 ON 10ms after start |
| 28 | 1 | 1 | 1 | 0 | 1 | 0 | 1 | 0 | 234 | Reg Power1 Ctrl | | CP SD3 SD2 DIG2 RF2 ON 11ms after start |
| 29 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 254 | Reg Power1 Ctrl | | CP SD3 SD2 SD1 DIG2 DIG1 RF2 ON 12ms after start |
| 30 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power2 Ctrl | | RF3_SW=0; Stpup2=0; Stpup1=0; RF2_sw=0; RF1_sw=0; DIG4_ON=0; DIG3_ON=0; RF3_ON=0; 13ms after start |
| 31 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg GPIO Ctrl | | DIG3_gpio=0; SD3_gpio=0; SD2_gpio=0; SD1_gpio=0; DIG2_gpio=0; DIG1_gpio=0; RF2_gpio=0; RF1_gpio=0; |

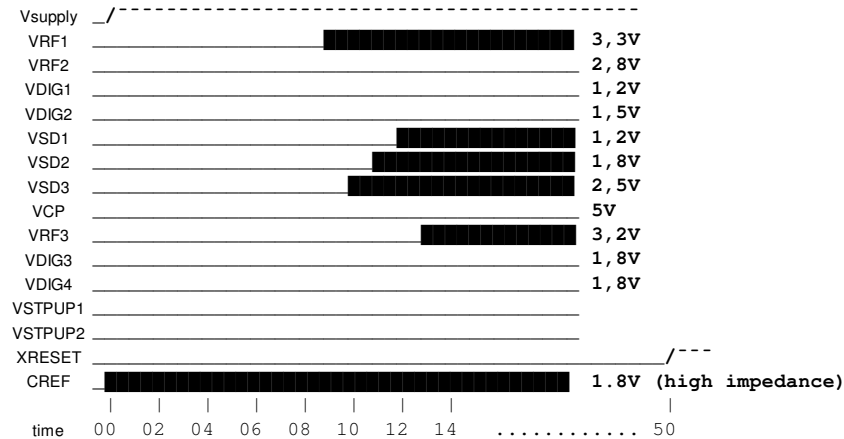
Rprogram = ∞ (open)



AS3658C Version=3.3

Boot-ROM Mapping Table for Code = xx / please only modify Data-Column

| ROM-Adr. | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | Data | Register | Section | Description |
|----------|----|----|----|----|----|----|----|----|------|-------------------------|-------------------|--|
| 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 12 | Step Down Voltage1 | Bank 2 | clkinv=0; frequ=2.2MHz; VBUCK=1,2V |
| 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 24 | Step Down Voltage2 | | clkinv=0; frequ=2.2MHz; VBUCK=1,8V |
| 2 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 0 | 38 | Step Down Voltage3 | | clkinv=0; frequ=2.2MHz; VBUCK=2,5V |
| 3 | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 125 | LDO_RF1 Voltage | | swprot_en=1; lcurr_en=1; VRF1=3,3V |
| 4 | 0 | 0 | 1 | 1 | 0 | 0 | 1 | 1 | 51 | LDO_RF2 Voltage | | double_reset=0; slow_startup=0; lcurr_en=1; VRF2=2,8V |
| 5 | 0 | 0 | 1 | 1 | 1 | 0 | 1 | 1 | 59 | LDO_RF3 Voltage | | rf3_hotplug_en=0; lcurr_en=1; VRF3=3,2V |
| 6 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 9 | LDO_DIG1 Voltage | | VDIG1=1,2V |
| 7 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 15 | LDO_DIG2 Voltage | | VDIG2=1,5V |
| 8 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 31 | LDO_DIG3 Voltage | | VDIG3=1,8V |
| 9 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 31 | LDO_DIG4 Voltage | | VDIG4=1,8V |
| 10 | 1 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 216 | USB Charger Control | | ext_batsw_en=1; no_charging=1; dis_batsw_temp_prot=0; usb_chg_en=1; usb_current=470mA |
| 11 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 173 | Charger Control1 | | isolate_bat=1; ch_det_500ms=0; chg_tmax=1; usb_hold_chdet=0; autoresume=1; CHOVDetEn=1; Ch_pwoff_en=0; ChEn=1; |
| 12 | 0 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 103 | Battery voltage monitor | | FastResEn=0; sup_res_en=1; ResFall=3,1V; ResRise=3,4V; |
| 13 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 0 | 38 | Charger Config | | ChVoltResume=3,9V; Vchargeoff=3,9V; chvoltagec=4,2V; |
| 14 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 16 | Charger supervision | | ntc_type=0; ntc_hyst=0; ntc_hightemp=0; autoshtdown=1; ch_timeout=off |
| 15 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | FuelGauge | | ntc_on=Disable NTC supervision; power_off_at_vsuplow=0; CalMod=0; CalReq=0; UpdReq=0; FGEn=0; |
| 16 | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 61 | ChargerCurrent | | Ch_Voltage=4,4V; const_current=35mV; TrickleCurrent=2,5mV; |
| 17 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | Charge Pump Control | | SD1ctrl=no 1A mode; sd1_dvm_time=0usec; "frequ=1.1MHz; Pulseskip=1; |
| 18 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO1 Control | Program = 320kOhm | no pulls; I/O mode; no invert; high impedance |
| 19 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO2 Control | | no pulls; I/O mode; no invert; high impedance |
| 20 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO3 Control | | no pulls; I/O mode; no invert; high impedance |
| 21 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO4 Control | | no pulls; I/O mode; no invert; high impedance |
| 22 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 11 | Reset Timer | | rtc_mode=off; rtc_rep_wakeup_en=0; rtc_alarm_wakeup_en=0; xon_enable=1; Res_Timer=50ms |
| 23 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 6ms after start |
| 24 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 7ms after start |
| 25 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 8ms after start |
| 26 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | Reg Power1 Ctrl | | RF1 ON 9ms after start |
| 27 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 65 | Reg Power1 Ctrl | | SD3 RF1 ON 10ms after start |
| 28 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 97 | Reg Power1 Ctrl | | SD3 SD2 RF1 ON 11ms after start |
| 29 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 113 | Reg Power1 Ctrl | | SD3 SD2 SD1 RF1 ON 12ms after start |
| 30 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | Reg Power2 Ctrl | | RF3_SW=0; Stpup2=0; Stpup1=0; RF2_sw=0; RF1_sw=0; DIG4_ON=0; DIG3_ON=0; RF3_ON=1; 13ms after start |
| 31 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg GPIO Ctrl | | DIG3_gpio=0; SD3_gpio=0; SD2_gpio=0; SD1_gpio=0; DIG2_gpio=0; DIG1_gpio=0; RF2_gpio=0; RF1_gpio=0; |

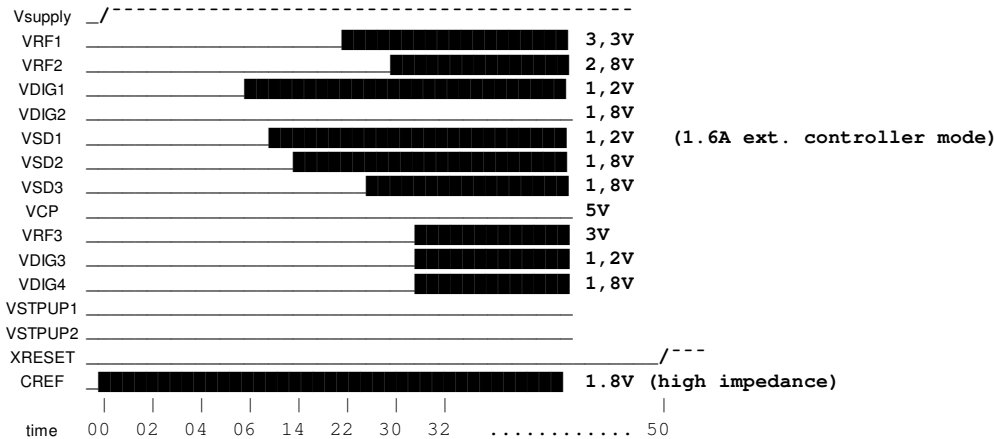


AS3658C Version=3.3

Boot-ROM Mapping Table for Code = xx / please only modify Data-Column

| ROM-Adr. | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | Data | Register | Section | Description |
|----------|----|----|----|----|----|----|----|----|------|-------------------------|---------|--|
| 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 12 | Step Down Voltage1 | Bank 3 | clkinv=0; frequ=2.2MHz; VBUCK=1.2V |
| 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 152 | Step Down Voltage2 | | clkinv=1; frequ=2.2MHz; VBUCK=1.8V |
| 2 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 24 | Step Down Voltage3 | | clkinv=0; frequ=2.2MHz; VBUCK=1.8V |
| 3 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 1 | 29 | LDO_RF1 Voltage | | swprot_en=0; lcurr_en=0; VRF1=3.3V |
| 4 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 1 | 83 | LDO_RF2 Voltage | | double_reset=0; slow_startup=1; lcurr_en=0; VRF2=2.8V |
| 5 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 23 | LDO_RF3 Voltage | | rf3_hotplug_en=0; lcurr_en=0; VRF3=3V |
| 6 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 9 | LDO_DIG1 Voltage | | VDIG1=1.2V |
| 7 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 21 | LDO_DIG2 Voltage | | VDIG2=1.8V |
| 8 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 9 | LDO_DIG3 Voltage | | VDIG3=1.2V |
| 9 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 22 | LDO_DIG4 Voltage | | VDIG4=1.8V |
| 10 | 1 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 216 | USB Charger Control | | ext_batsw_en=1; no_charging=1; dis_batsw_temp_prot=0; usb_chg_en=1; usb_current=470mA |
| 11 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 173 | Charger Control1 | | isolate_bat=1; ch_det_500ms=0; chg_tmax=1; usb_hold_chdet=0; autoresume=1; CHOVDetEn=1; Ch_pwoff_en=0; ChEn=1; |
| 12 | 0 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 103 | Battery voltage monitor | | FastResEn=0; sup_res_en=1; ResFall=3.1V; ResRise=3.4V; |
| 13 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 0 | 38 | Charger Config | | ChVoltresum=3.9V; Vchargeoff=3.9V; chvolteoc=4.2V; |
| 14 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 16 | Charger supervision | | ntc_type=0; ntc_hyst=0; ntc_hightemp=0; autoshutdown=1; ch_timeout=off |
| 15 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | FuelGauge | | ntc_on=Disable NTC supervision; power_off_at_vsuplow=0; CalMod=0; CalReq=0; UpdReq=0; FGEn=0; |
| 16 | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 61 | ChargerCurrent | | Ch_Voltage=4.4V; const_current=35mV; TrickleCurrent=2.5mV; |
| 17 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 193 | Charge Pump Control | | SD1ctrl=1.6A controller mode; sd1_dvm_time=0usec; frequ=1.1MHz; Pulseskip=1; |
| 18 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO1 Control | | no pulls; I/O mode; no invert; high impedance |
| 19 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO2 Control | | no pulls; I/O mode; no invert; high impedance |
| 20 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 82 | GPIO3 Control | | pull-down in input mode; GPIO3 control of Regs.; no invert; Digital input |
| 21 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 82 | GPIO4 Control | | pull-down in input mode; GPIO4 control of Regs.; no invert; Digital input |
| 22 | 1 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 139 | Reset Timer | | rtc_mode=Pin Q32k enabled; rtc_rep_wakeup_en=0; rtc_alarm_wakeup_en=0; xon_enable=1; Res_Timer=50ms |
| 23 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 4 | Reg Power1 Ctrl | | DIG1 ON 6ms after start |
| 24 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 20 | Reg Power1 Ctrl | | SD1 DIG1 ON 7ms after start |
| 25 | 0 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 52 | Reg Power1 Ctrl | | SD2 SD1 DIG1 ON 8ms after start |
| 26 | 0 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 52 | Reg Power1 Ctrl | | SD2 SD1 DIG1 ON 9ms after start |
| 27 | 0 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 53 | Reg Power1 Ctrl | | SD2 SD1 DIG1 RF1 ON 10ms after start |
| 28 | 0 | 1 | 1 | 1 | 0 | 1 | 0 | 1 | 117 | Reg Power1 Ctrl | | SD3 SD2 SD1 DIG1 RF1 ON 11ms after start |
| 29 | 0 | 1 | 1 | 1 | 0 | 1 | 1 | 1 | 119 | Reg Power1 Ctrl | | SD3 SD2 SD1 DIG1 RF2 RF1 ON 12ms after start |
| 30 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | Reg Power2 Ctrl | | RF3_SW=0; Stpup2=0; Stpup1=0; RF2_sw=0; RF1_sw=0; DIG4_ON=1; DIG3_ON=1; RF3_ON=1; 13ms after start |
| 31 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg GPIO Ctrl | | DIG3_gpio=0; SD3_gpio=0; SD2_gpio=0; SD1_gpio=0; DIG2_gpio=0; DIG1_gpio=0; RF2_gpio=0; RF1_gpio=0; |

Rprogram = 160kOhm

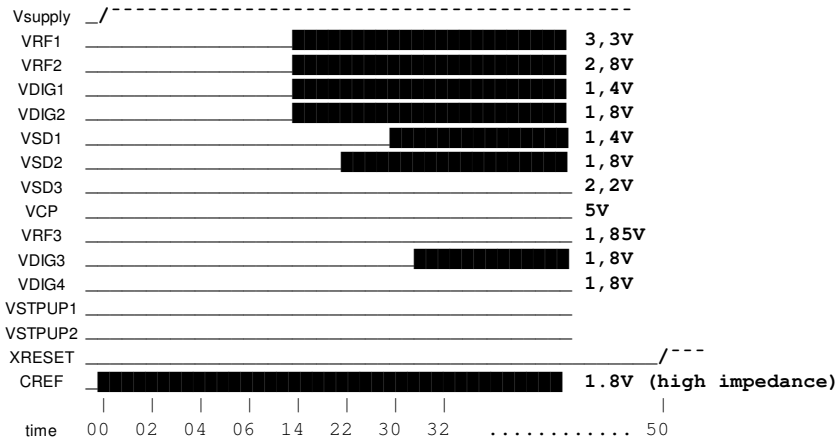


AS3658C Version=3.3

Boot-ROM Mapping Table for Code = xx / please only modify Data-Column

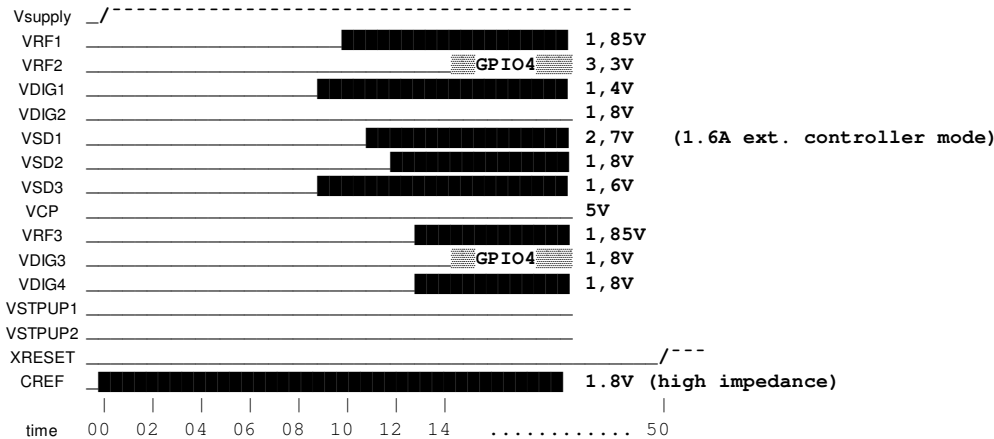
| ROM-Adr. | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | Data | Register | Section | Description |
|----------|----|----|----|----|----|----|----|----|------|-------------------------|---------|--|
| 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 80 | Step Down Voltage1 | Bank 4 | clkinv=0; frequ=1.1MHz; VBUCK=1.4V |
| 1 | 1 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 216 | Step Down Voltage2 | | clkinv=1; frequ=1.1MHz; VBUCK=1.8V |
| 2 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 96 | Step Down Voltage3 | | clkinv=0; frequ=1.1MHz; VBUCK=2.2V |
| 3 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 1 | 29 | LDO_RF1 Voltage | | swprot_en=0; lcurr_en=0; VRF1=3.3V |
| 4 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 1 | 83 | LDO_RF2 Voltage | | double_reset=0; slow_startup=1; lcurr_en=0; VRF2=2.8V |
| 5 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | LDO_RF3 Voltage | | rf3_hotplug_en=0; lcurr_en=0; VRF3=1.85V |
| 6 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 1 | 13 | LDO_DIG1 Voltage | | VDIG1=1.4V |
| 7 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 21 | LDO_DIG2 Voltage | | VDIG2=1.8V |
| 8 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 21 | LDO_DIG3 Voltage | | VDIG3=1.8V |
| 9 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 21 | LDO_DIG4 Voltage | | VDIG4=1.8V |
| 10 | 1 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 216 | USB Charger Control | | ext_batsw_en=1; no_charging=1; dis_batsw_temp_prot=0; usb_chg_en=1; usb_current=470mA |
| 11 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 173 | Charger Control1 | | isolate_bat=1; ch_det_500ms=0; chg_tmax=1; usb_hold_chdet=0; autoresume=1; CHOVDetEn=1; Ch_pwoff_en=0; ChEn=1; |
| 12 | 0 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 103 | Battery voltage monitor | | FastResEn=0; sup_res_en=1; ResFall=3.1V; ResRise=3.4V; |
| 13 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 0 | 38 | Charger Config | | ChVoltResume=3.9V; Vchargeoff=3.9V; chvoltagec=4.2V; |
| 14 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 16 | Charger supervision | | ntc_type=0; ntc_hyst=0; ntc_hightemp=0; autoshutdown=1; ch_timeout=off |
| 15 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | FuelGauge | | ntc_on=Disable NTC supervision; power_off_at_vsuplow=0; CalMod=0; CalReq=0; UpdReq=0; FGEn=0; |
| 16 | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 61 | ChargerCurrent | | Ch_Voltage=4.4V; const_current=35mV; TrickleCurrent=2.5mV; |
| 17 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | Charge Pump Control | | SD1ctrl=no 1A mode; sd1_dvm_time=0usec; "frequ=1.1MHz; Pulseskip=1; |
| 18 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO1 Control | | no pulls; I/O mode; no invert; high impedance |
| 19 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO2 Control | | no pulls; I/O mode; no invert; high impedance |
| 20 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO3 Control | | no pulls; I/O mode; no invert; high impedance |
| 21 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 82 | GPIO4 Control | | pull-down in input mode; GPIO4 control of Regs.; no invert; Digital input |
| 22 | 1 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 139 | Reset Timer | | rtc_mode=Pin Q32k enabled; rtc_rep_wakeup_en=0; rtc_alarm_wakeup_en=0; xon_enable=1; Res_Timer=50ms |
| 23 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 6ms after start |
| 24 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 7ms after start |
| 25 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 15 | Reg Power1 Ctrl | | DIG2 DIG1 RF2 RF1 ON 8ms after start |
| 26 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 15 | Reg Power1 Ctrl | | DIG2 DIG1 RF2 RF1 ON 9ms after start |
| 27 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 47 | Reg Power1 Ctrl | | SD2 DIG2 DIG1 RF2 RF1 ON 10ms after start |
| 28 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 47 | Reg Power1 Ctrl | | SD2 DIG2 DIG1 RF2 RF1 ON 11ms after start |
| 29 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 63 | Reg Power1 Ctrl | | SD2 SD1 DIG2 DIG1 RF2 RF1 ON 12ms after start |
| 30 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | Reg Power2 Ctrl | | RF3_SW=0; Stpup2=0; Stpup1=0; RF2_sw=0; RF1_sw=0; DIG4_ON=0; DIG3_ON=1; RF3_ON=0; 13ms after start |
| 31 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg GPIO Ctrl | | DIG3_gpio=0; SD3_gpio=0; SD2_gpio=0; SD1_gpio=0; DIG2_gpio=0; DIG1_gpio=0; RF2_gpio=0; RF1_gpio=0; |

Rpprogram = 80kOhm



Boot-ROM Mapping Table for Code = xx / please only modify Data-Column

| ROM-Adr. | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | Data | Register | Section | Description |
|----------|----|----|----|----|----|----|----|----|------|-------------------------|--|--|
| 0 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 0 | 106 | Step Down Voltage1 | Bank 5 | clkinv=0; frequ=1.1MHz; VBUCK=2,7V |
| 1 | 1 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 216 | Step Down Voltage2 | | clkinv=1; frequ=1.1MHz; VBUCK=1,8V |
| 2 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 20 | Step Down Voltage3 | | clkinv=0; frequ=2,2MHz; VBUCK=1,6V |
| 3 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | LDO_RF1 Voltage | | swprot_en=0; lcurr_en=0; VRF1=1,85V |
| 4 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 1 | 29 | LDO_RF2 Voltage | | double_reset=0; slow_startup=0; lcurr_en=0; VRF2=3,3V |
| 5 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | LDO_RF3 Voltage | | rf3_hotplug_en=0; lcurr_en=0; VRF3=1,85V |
| 6 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 1 | 13 | LDO_DIG1 Voltage | | VDIG1=1,4V |
| 7 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 21 | LDO_DIG2 Voltage | | VDIG2=1,8V |
| 8 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 21 | LDO_DIG3 Voltage | | VDIG3=1,8V |
| 9 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 21 | LDO_DIG4 Voltage | | VDIG4=1,8V |
| 10 | 1 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 216 | USB Charger Control | | ext_batsw_en=1; no_charging=1; dis_batsw_temp_prot=0; usb_chg_en=1; usb_current=470mA |
| 11 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 173 | Charger Control1 | | isolate_bat=1; ch_det_500ms=0; chg_tmax=1; usb_hold_chdet=0; autoresume=1; CHOVDetEn=1; Ch_pwoff_en=0; ChEn=1; |
| 12 | 0 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 103 | Battery voltage monitor | | FastResEn=0; sup_res_en=1; ResFall=3,1V; ResRise=3,4V; |
| 13 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 0 | 38 | Charger Config | | ChVoltresum=3,9V; Vchargeoff=3,9V; chvolteoc=4,2V; |
| 14 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Charger supervision | | ntc_type=0; ntc_hyst=0; ntc_hightemp=0; autoshutdown=0; ch_timeout=off |
| 15 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | FuelGauge | | ntc_on=Disable NTC supervision; power_off_at_vsuplow=0; CalMod=0; CalReq=0; UpdReq=0; FGEn=0; |
| 16 | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 61 | ChargerCurrent | | Ch_Voltage=4,4V; const_current=35mV; TrickleCurrent=2,5mV; |
| 17 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 193 | Charge Pump Control | | SD1ctrl=1.6A controller mode; sd1_dvm_time=0usec; frequ=1.1MHz; Pulseskip=1; |
| 18 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO1 Control | Rprogram = 40kOhm | no pulls; I/O mode; no invert; high impedance |
| 19 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO2 Control | | no pulls; I/O mode; no invert; high impedance |
| 20 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO3 Control | | no pulls; I/O mode; no invert; high impedance |
| 21 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 82 | GPIO4 Control | | pull-down in input mode; GPIO4 control of Regs.; no invert; Digital input |
| 22 | 1 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 139 | Reset Timer | | rtc_mode=Pin Q32k enabled; rtc_rep_wakeup_en=0; rtc_alarm_wakeup_en=0; xon_enable=1; Res_Timer=50ms |
| 23 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 6ms after start |
| 24 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 7ms after start |
| 25 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 8ms after start |
| 26 | 0 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 68 | Reg Power1 Ctrl | | SD3 DIG1 ON 9ms after start |
| 27 | 0 | 1 | 0 | 0 | 0 | 1 | 0 | 1 | 69 | Reg Power1 Ctrl | | SD3 DIG1 RF1 ON 10ms after start |
| 28 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 85 | Reg Power1 Ctrl | SD3 SD1 DIG1 RF1 ON 11ms after start | |
| 29 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 117 | Reg Power1 Ctrl | SD3 SD2 SD1 DIG1 RF1 ON 12ms after start | |
| 30 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 5 | Reg Power2 Ctrl | RF3_SW=0; Stpup2=0; Stpup1=0; RF2_sw=0; RF1_sw=0; DIG4_ON=1; DIG3_ON=0; RF3_ON=1; 13ms after start | |
| 31 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 130 | Reg GPIO Ctrl | DIG3_gpio=1; SD3_gpio=0; SD2_gpio=0; SD1_gpio=0; DIG2_gpio=0; DIG1_gpio=0; RF2_gpio=1; RF1_gpio=0; | |

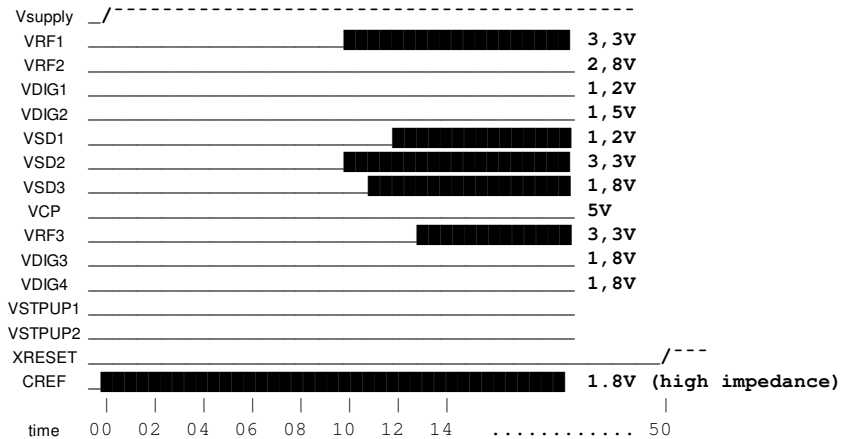


AS3658C Version=3.3

AS3530

Boot-ROM Mapping Table for Code = xx / please only modify Data-Column

| ROM-Adr. | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | Data | Register | Section | Description |
|----------|----|----|----|----|----|----|----|----|------|-------------------------|-------------------|--|
| 0 | 0 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 76 | Step Down Voltage1 | Bank 6 | clkinv=0; frequ=1.1MHz; VBUCK=1,2V |
| 1 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 0 | 246 | Step Down Voltage2 | | clkinv=1; frequ=1.1MHz; VBUCK=3,3V |
| 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 88 | Step Down Voltage3 | | clkinv=0; frequ=1.1MHz; VBUCK=1,8V |
| 3 | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 125 | LDO_RF1 Voltage | | swprot_en=1; lcurr_en=1; VRF1=3,3V |
| 4 | 0 | 0 | 1 | 1 | 0 | 0 | 1 | 1 | 51 | LDO_RF2 Voltage | | double_reset=0; slow_startup=0; lcurr_en=1; VRF2=2,8V |
| 5 | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 61 | LDO_RF3 Voltage | | rf3_hotplug_en=0; lcurr_en=1; VRF3=3,3V |
| 6 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 9 | LDO_DIG1 Voltage | | VDIG1=1,2V |
| 7 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 15 | LDO_DIG2 Voltage | | VDIG2=1,5V |
| 8 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 31 | LDO_DIG3 Voltage | | VDIG3=1,8V |
| 9 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 31 | LDO_DIG4 Voltage | | VDIG4=1,8V |
| 10 | 1 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 216 | USB Charger Control | | ext_batsw_en=1; no_charging=1; dis_batsw_temp_prot=0; usb_chg_en=1; usb_current=470mA |
| 11 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 173 | Charger Control1 | | isolate_bat=1; ch_det_500ms=0; chg_tmax=1; usb_hold_chdet=0; autoresume=1; CHOVDetEn=1; Ch_pwoff_en=0; ChEn=1; |
| 12 | 0 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 103 | Battery voltage monitor | | FastResEn=0; sup_res_en=1; ResFall=3,1V; ResRise=3,4V; |
| 13 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 0 | 38 | Charger Config | | ChVoltresume=3,9V; Vchargeoff=3,9V; chvolteoc=4,2V; |
| 14 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 16 | Charger supervision | | ntc_type=0; ntc_hyst=0; ntc_hightemp=0; autoshutdown=1; ch_timeout=off |
| 15 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | FuelGauge | | ntc_on=Disable NTC supervision; power_off_at_vsulpw=0; CalMod=0; CalReq=0; UpdReq=0; FGEn=0; |
| 16 | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 61 | ChargerCurrent | | Ch_Voltage=4,4V; const_current=35mV; TrickleCurrent=2,5mV; |
| 17 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | Charge Pump Control | | SD1ctrl=no 1A mode; sd1_dvm_time=0usec; "frequ=1.1MHz; Pulseskip=1; |
| 18 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO1 Control | Rprogram = 20kOhm | no pulls; I/O mode; no invert; high impedance |
| 19 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO2 Control | | no pulls; I/O mode; no invert; high impedance |
| 20 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO3 Control | | no pulls; I/O mode; no invert; high impedance |
| 21 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO4 Control | | no pulls; I/O mode; no invert; high impedance |
| 22 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 11 | Reset Timer | | rtc_mode=off; rtc_rep_wakeup_en=0; rtc_alarm_wakeup_en=0; xon_enable=1; Res_Timer=50ms |
| 23 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 6ms after start |
| 24 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 7ms after start |
| 25 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 8ms after start |
| 26 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 9ms after start |
| 27 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 33 | Reg Power1 Ctrl | | SD2 RF1 ON 10ms after start |
| 28 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 97 | Reg Power1 Ctrl | | SD3 SD2 RF1 ON 11ms after start |
| 29 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 113 | Reg Power1 Ctrl | | SD3 SD2 SD1 RF1 ON 12ms after start |
| 30 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | Reg Power2 Ctrl | | RF3_SW=0; Stpup2=0; Stpup1=0; RF2_sw=0; RF1_sw=0; DIG4_ON=0; DIG3_ON=0; RF3_ON=1; 13ms after start |
| 31 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg GPIO Ctrl | | DIG3_gpio=0; SD3_gpio=0; SD2_gpio=0; SD1_gpio=0; DIG2_gpio=0; DIG1_gpio=0; RF2_gpio=0; RF1_gpio=0; |

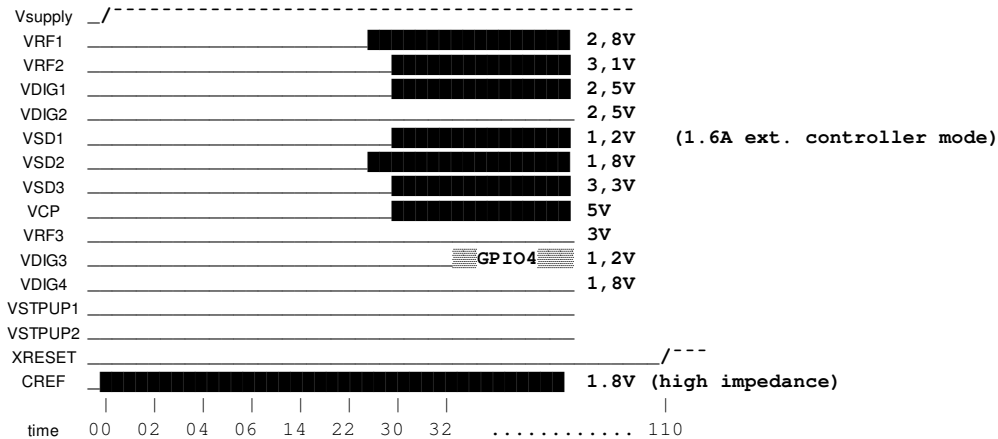


AS3658C Version=3.3

Boot-ROM Mapping Table for Code = xx / please only modify Data-Column

| ROM-Adr. | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | Data | Register | Section | Description |
|----------|----|----|----|----|----|----|----|----|------|-------------------------|---------|---|
| 0 | 0 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 76 | Step Down Voltage1 | Bank 7 | clkinv=0; frequ=1.1MHz; VBUCK=1,2V |
| 1 | 1 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 216 | Step Down Voltage2 | | clkinv=1; frequ=1.1MHz; VBUCK=1,8V |
| 2 | 0 | 1 | 1 | 1 | 0 | 1 | 1 | 0 | 118 | Step Down Voltage3 | | clkinv=0; frequ=1.1MHz; VBUCK=3,3V |
| 3 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 19 | LDO_RF1 Voltage | | swprot_en=0; lcurr_en=0; VRF1=2,8V |
| 4 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 1 | 89 | LDO_RF2 Voltage | | double_reset=0; slow_startup=1; lcurr_en=0; VRF2=3,1V |
| 5 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 23 | LDO_RF3 Voltage | | rf3_hotplug_en=0; lcurr_en=0; VRF3=3V |
| 6 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 42 | LDO_DIG1 Voltage | | VDIG1=2,5V |
| 7 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 42 | LDO_DIG2 Voltage | | VDIG2=2,5V |
| 8 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 9 | LDO_DIG3 Voltage | | VDIG3=1,2V |
| 9 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 21 | LDO_DIG4 Voltage | | VDIG4=1,8V |
| 10 | 1 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 216 | USB Charger Control | | ext_batsw_en=1; no_charging=1; dis_batsw_temp_prot=0; usb_chg_en=1; usb_current=470mA |
| 11 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 173 | Charger Control1 | | isolate_bat=1; ch_det_500ms=0; chg_tmax=1; usb_hold_chdet=0; autoresume=1; CHOVDetEn=1; Ch_pwroff_en=0; ChEn=1; |
| 12 | 0 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 103 | Battery voltage monitor | | FastResEn=0; sup_res_en=1; ResFall=3,1V; ResRise=3,4V; |
| 13 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 0 | 38 | Charger Config | | ChVoltResume=3,9V; Vchargeoff=3,9V; chvolteoc=4,2V; |
| 14 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 16 | Charger supervision | | ntc_type=0; ntc_hyst=0; ntc_hightemp=0; autoshtutdown=1; ch_timeout=off |
| 15 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | FuelGauge | | ntc_on=Disable NTC supervision; power_off_at_vsuplow=0; CalMod=0; CalReq=0; UpdReq=0; FGEn=0; |
| 16 | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 61 | ChargerCurrent | | Ch_Voltage=4,4V; const_current=35mV; TrickleCurrent=2,5mV; |
| 17 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 193 | Charge Pump Control | | SD1ctrl=1,6A controller mode; sd1_dvm_time=0usec; frequ=1.1MHz; Pulseskip=1; |
| 18 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO1 Control | | no pulls; I/O mode; no invert; high impedance |
| 19 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO2 Control | | no pulls; I/O mode; no invert; high impedance |
| 20 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO3 Control | | no pulls; I/O mode; no invert; high impedance |
| 21 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 18 | GPIO4 Control | | no pulls; GPIO4 control of Regs.; no invert; Digital input |
| 22 | 1 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 143 | Reset Timer | | rtc_mode=Pin Q32k enabled; rtc_rep_wakeup_en=0; rtc_alarm_wakeup_en=0; xon_enable=1; Res_Timer=110ms |
| 23 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 6ms after start |
| 24 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 7ms after start |
| 25 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 8ms after start |
| 26 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 9ms after start |
| 27 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 10ms after start |
| 28 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 33 | Reg Power1 Ctrl | | SD2 RF1 ON 11ms after start |
| 29 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 1 | 247 | Reg Power1 Ctrl | | CP SD3 SD2 SD1 DIG1 RF2 RF1 ON 12ms after start |
| 30 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power2 Ctrl | | RF3_SW=0; Stpup2=0; Stpup1=0; RF2_sw=0; RF1_sw=0; DIG4_ON=0; DIG3_ON=0; RF3_ON=0; 13ms after start |
| 31 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 128 | Reg GPIO Ctrl | | DIG3_gpio=1; SD3_gpio=0; SD2_gpio=0; SD1_gpio=0; DIG2_gpio=0; DIG1_gpio=0; RF2_gpio=0; RF1_gpio=0; |

Rprogram = 10kOhm



AS3658C Version=3.3

Sequence Production Test -- DO NOT CHANGE

Boot-ROM Mapping Table for Code = xx / please only modify Data-Column

| ROM-Adr. | D7 | D6 | D5 | D4 | D3 | D2 | D1 | D0 | Data | Register | Section | Description |
|----------|----|----|----|----|----|----|----|----|------|-------------------------|---------|---|
| 0 | 0 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 76 | Step Down Voltage1 | Bank 8 | clkinv=0; frequ=1.1MHz; VBUCK=1.2V |
| 1 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 204 | Step Down Voltage2 | | clkinv=1; frequ=1.1MHz; VBUCK=1.2V |
| 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 88 | Step Down Voltage3 | | clkinv=0; frequ=1.1MHz; VBUCK=1.8V |
| 3 | | 0 | 0 | 0 | 1 | 1 | 0 | 1 | 13 | LDO RF1 Voltage | | swprot_en=0; lcurr_en=0; VRF1=2.5V |
| 4 | | | 0 | 1 | 1 | 1 | 0 | 1 | 29 | LDO RF2 Voltage | | lcurr_en=0; VRF2=3.3V |
| 5 | | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 17 | LDO RF3 Voltage | | rf3_hotplug_en=0; lcurr_en=0; VRF3=2.7V |
| 6 | | | 0 | 1 | 0 | 1 | 0 | 1 | 21 | LDO DIG1 Voltage | | VDIG1=1.8V |
| 7 | | | 0 | 1 | 0 | 1 | 0 | 1 | 21 | LDO DIG2 Voltage | | VDIG2=1.8V |
| 8 | | | 0 | 0 | 1 | 0 | 0 | 1 | 9 | LDO DIG3 Voltage | | VDIG3=1.2V |
| 9 | | | 0 | 0 | 1 | 0 | 0 | 1 | 9 | LDO DIG4 Voltage | | VDIG4=1.2V |
| 10 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 88 | USB Charger Control | | ext_batsw_en=0; no_charging=1; dis_batsw_temp_prot=0; usb_chg_en=1; usb_current=470mA |
| 11 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 173 | Charger Control1 | | isolate_bat=1; ch_det_500ms=0; chg_tmax=1; usb_hold_chdet=0; autoresume=1; CHOVDetEn=1; Ch_pwroff_en=0; ChEn=1; |
| 12 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 1 | 29 | Battery voltage monitor | | FastResEn=0; sup_res_en=0; ResFall=3V; ResRise=3.2V; |
| 13 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 0 | 38 | Charger Config | | ChVoltresume=3.9V; Vchargeoff=3.9V; chvoltage=4.2V; |
| 14 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Charger supervision | | ntc_type=0; ntc_hyst=0; ntc_hightemp=0; autoshutdown=0; ch_timeout=off |
| 15 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | FuelGauge | | ntc_on=Disable NTC supervision; power_off_at_vsplow=0; CalMod=0; CalReq=0; UpdReq=0; FGEN=0; |
| 16 | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 61 | ChargerCurrent | | Ch_Voltage=4.4V; const_current=35mV; TrickleCurrent=2.5mV; |
| 17 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | Charge Pump Control | | SD1ctrl=no 1A mode; sd1_dvm_time=0usec; frequ=1.1MHz; Pulseskip=1; |
| 18 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO1 Control | | no pulls; I/O mode; no invert; high impedance |
| 19 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO2 Control | | no pulls; I/O mode; no invert; high impedance |
| 20 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO3 Control | | no pulls; I/O mode; no invert; high impedance |
| 21 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 7 | GPIO4 Control | | no pulls; I/O mode; no invert; high impedance |
| 22 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reset Timer | | rtc_mode=off; rtc_rep_wakeup_en=0; rtc_alarm_wakeup_en=0; xon_enable=0; Res_Timer=10ms |
| 23 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 6ms after start |
| 24 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 7ms after start |
| 25 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 8ms after start |
| 26 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 9ms after start |
| 27 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 10ms after start |
| 28 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 11ms after start |
| 29 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power1 Ctrl | | all regulators OFF 12ms after start |
| 30 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg Power2 Ctrl | | RF3_SW=0; Stpup2=0; Stpup1=0; RF2_sw=0; RF1_sw=0; DIG4_ON=0; DIG3_ON=0; RF3_ON=0; 13ms after start |
| 31 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | Reg GPIO Ctrl | | DIG3_gpio=0; SD3_gpio=0; SD2_gpio=0; SD1_gpio=0; DIG2_gpio=0; DIG1_gpio=0; RF2_gpio=0; RF1_gpio=0; |

Rprogram = 0x0mm

